

Electronic Acknowledgement Receipt

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Title of Invention:	Variable gain amplification circuit
First Named Inventor/Applicant Name:	Katsumasa Hijikata
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		AttachA_RES_REMARK_P03145001.pdf	237249 d48eda4ba57d7c36131cb2fabfe62f6/d9f7f2dd	yes	8

	Multipart Description/PDF files in .zip description		
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	Claims	2	5
	Applicant Arguments/Remarks Made in an Amendment	6	8
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